CLAIM AMENDMENTS

1.   (Previously Presented) A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

in response to the indication of the thermal event, controlling a signal associated with a mechanical power switch of the computer system to cause circuitry of the computer system to shut down the computer system in response to the signal indicating that the switch has been held in an off position for a predetermined delay; and

in response to the thermal event, powering down the processor before the signal indicates the switch has been held in the off position for the predetermined delay.

2.   (Previously Presented) The method of claim 1, wherein components of the computer system other than the processor are powered down after the signal indicates the switch has been in the off position for the predetermined delay.

3.   (Previously Presented) The method of claim 2, further comprising:

wherein said other components are located on a motherboard of the computer system.

4.   (Cancelled)

5.   (Original) The method of claim 1, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

6.   (Previously Presented) The method of claim 2, wherein said powering down other components comprises:

cutting off at least one supply voltage to said other components.

2

7.   (Previously Presented) A computer system comprising:

a processor capable of indicating a thermal event;

a mechanical switch, the switch being associated with a signal indicative of whether the switch has been held in an off position for a predetermined delay;

power consuming components;

a power supply subsystem to supply power to the processor and power consuming components; and

a circuit to:

in response to the indication of the thermal event, power down the processor before the signal indicates the switch has been in the off position for the predetermined delay, and

power down the power consuming components in response to the signal.

8.   (Original) The computer system of claim 7, wherein said power consuming components are located on a motherboard of the computer system.

9.   (Previously Presented) The computer system of claim 7, wherein the computer system introduces a delay in powering down said power consuming components.

10.   (Cancelled)

11.   (Original) The computer system of claim 7, wherein the power supply subsystem powers down the processor by cutting off a supply voltage to the processor.

12.   (Previously Presented) The computer system of claim 7, wherein the power supply subsystem powers down the power consuming components by cutting off at least one supply voltage to said power consuming components.

3

13.   (Previously Presented) A method comprising:

receiving an indication of a thermal event in a processor, the processor being part of a computer system;

in response to the indication, introducing a delay;

determining whether a mechanical power switch has been in an off position for the duration of the delay;

in response to the indication, powering down the processor before the expiration of the delay; and

powering down other components of the computer in response to the expiration of the delay.

14.   (Original) The method of claim 13, wherein said other components are located on a motherboard of the computer system.

15.   (Original) The method of claim 13, wherein said powering down other components comprises:

controlling a state of a signal indicative of a mechanical power switch of the computer system.

16.   (Original) The method of claim 13, wherein said powering down the processor comprises:

cutting off a supply voltage to the processor.

17.   (Original) The method of claim 13, wherein said powering down other components comprises:

cutting off at least one supply voltage to said other components.

18.-26. (Cancelled)

4